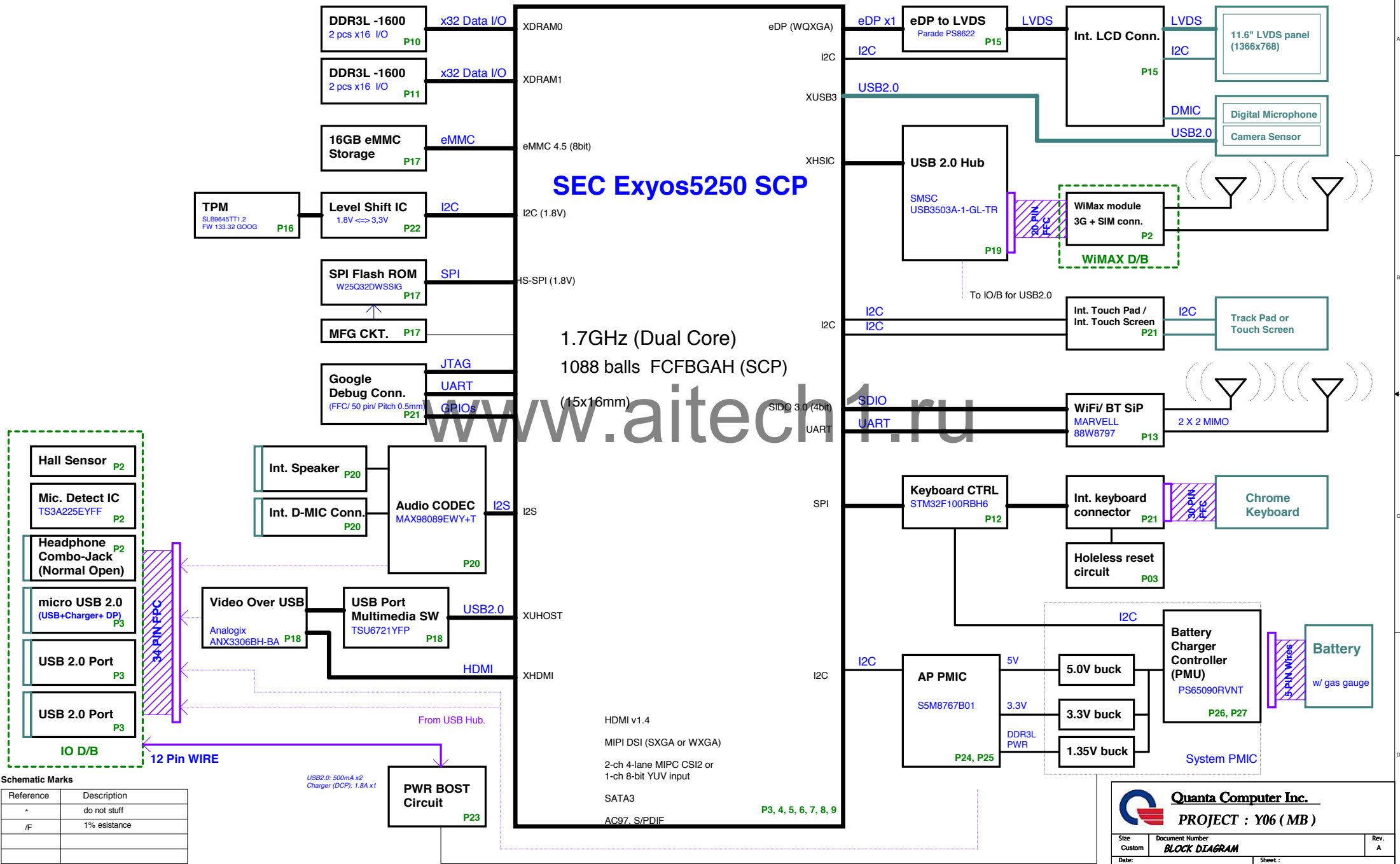
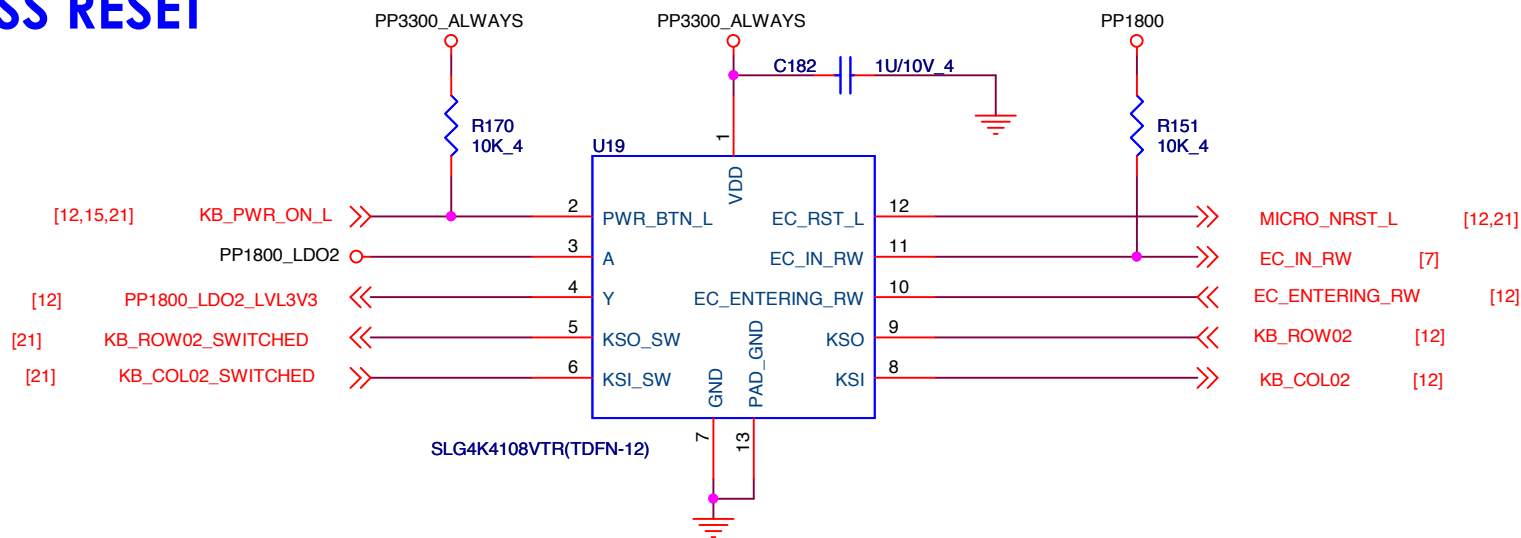


Block Diagram (SEC EXYNOS5250 SCP+ DDR3L)

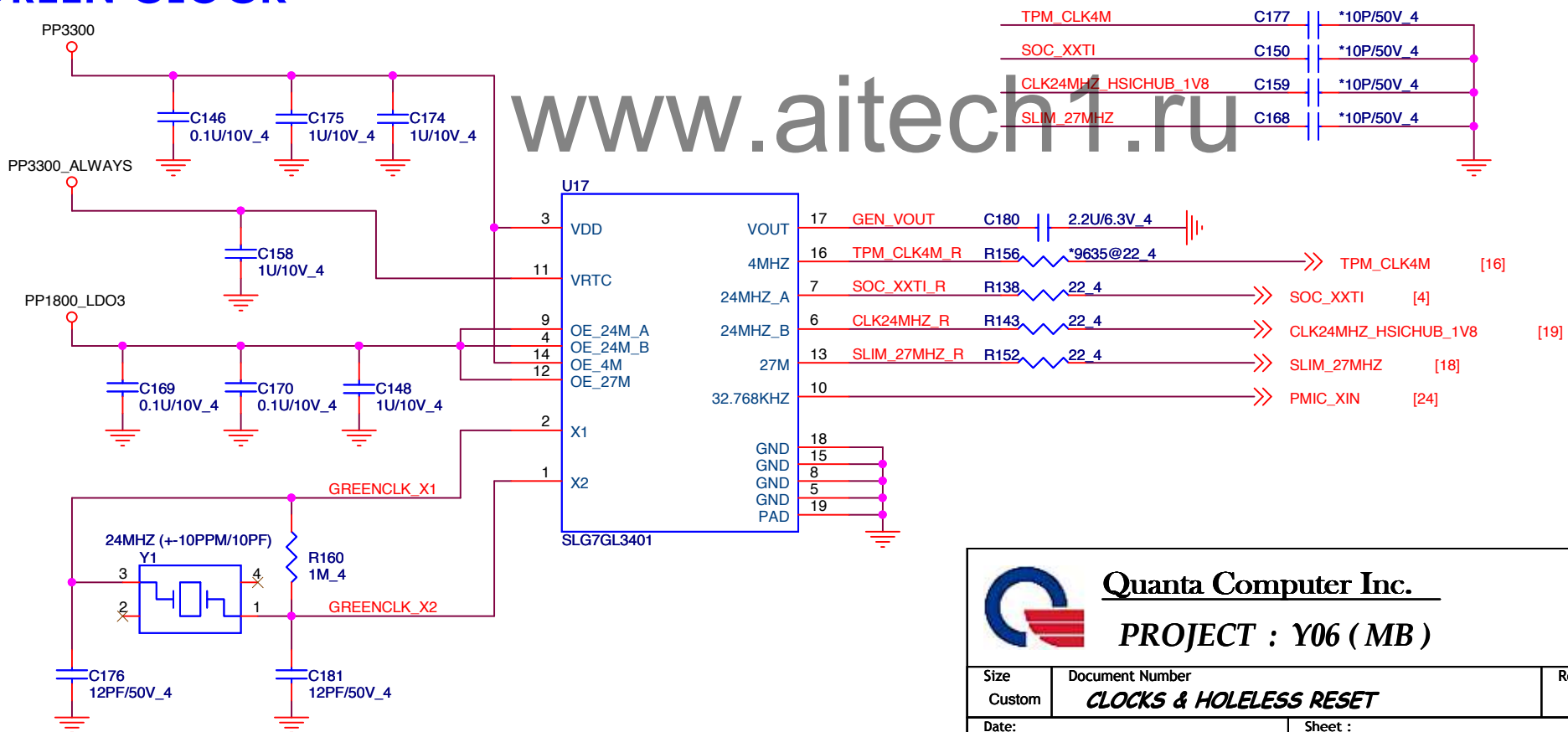




HOLELESS RESET



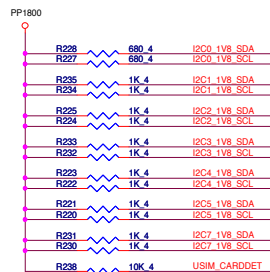
GREEN CLOCK



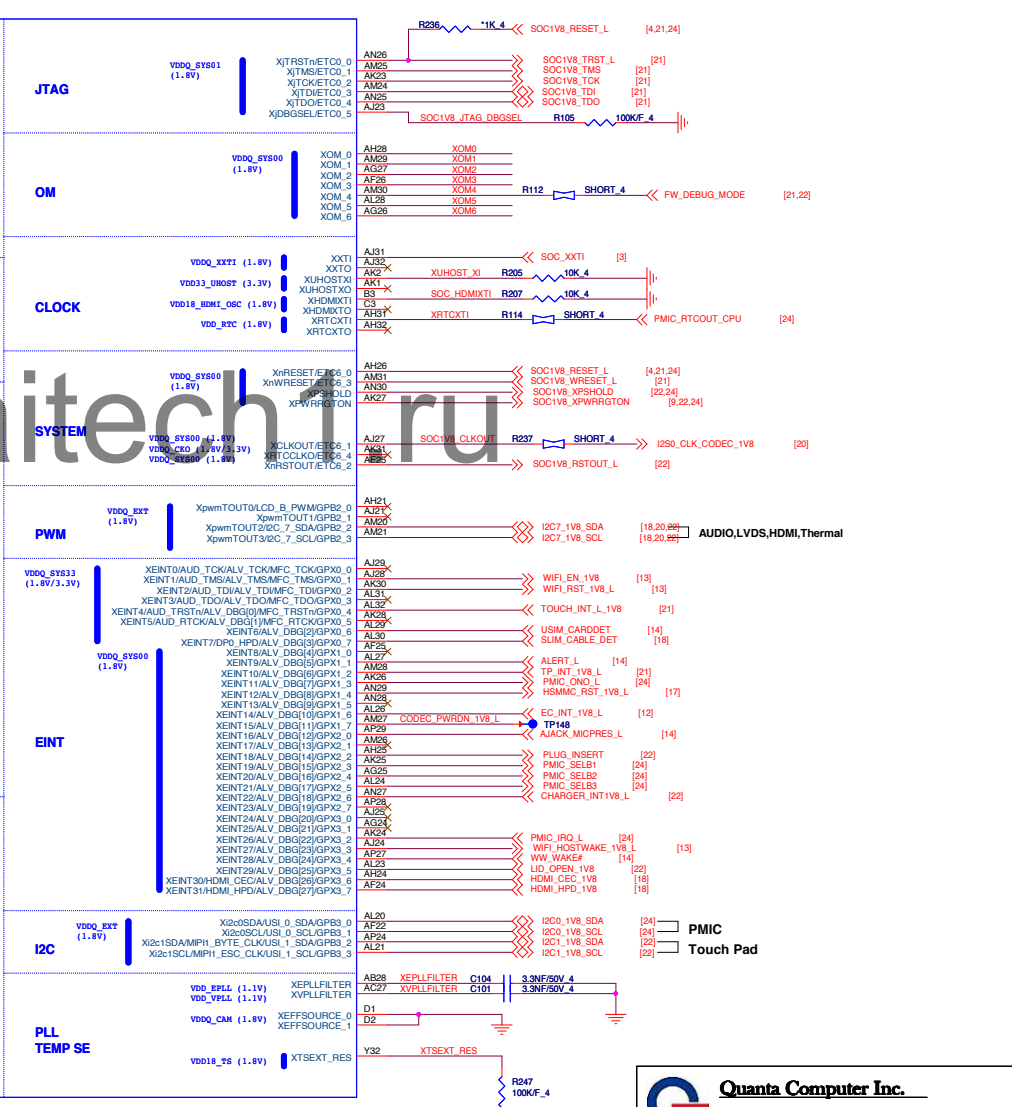
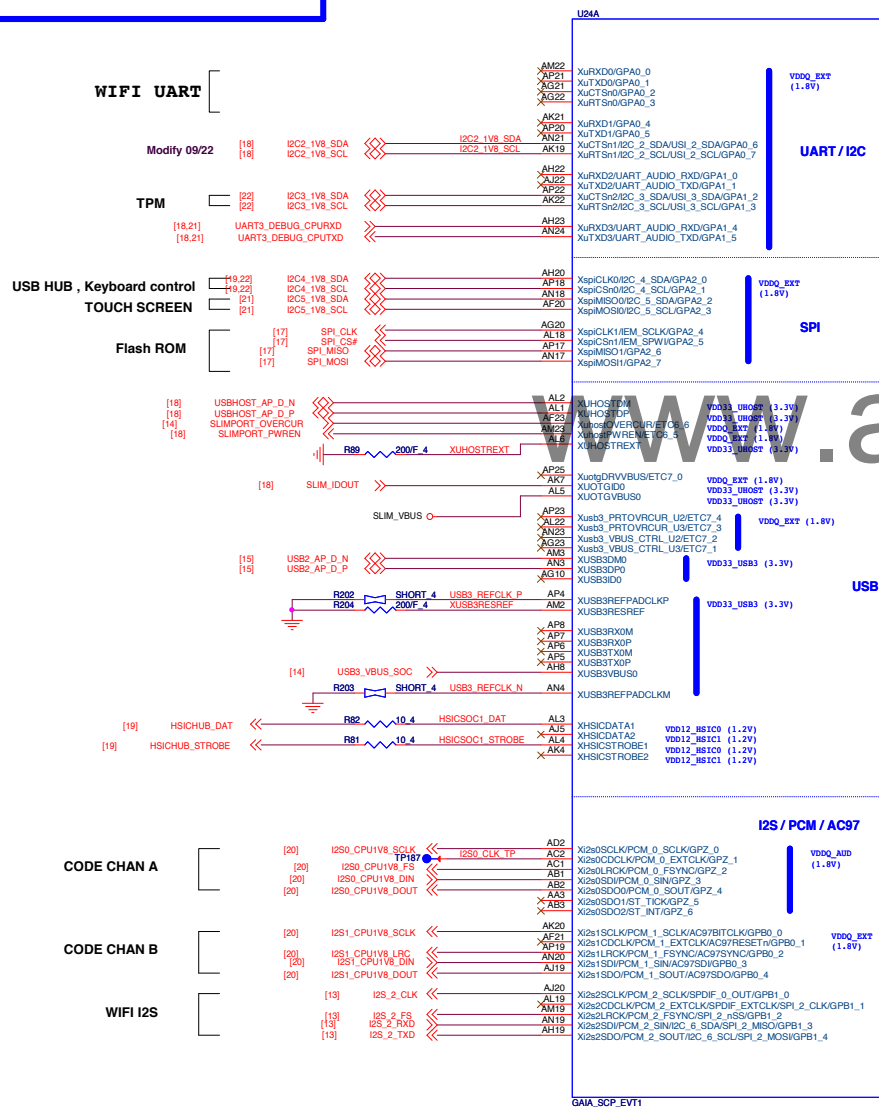
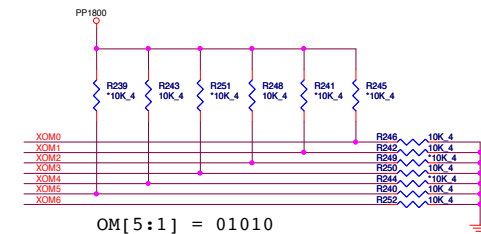
Quanta Computer Inc.

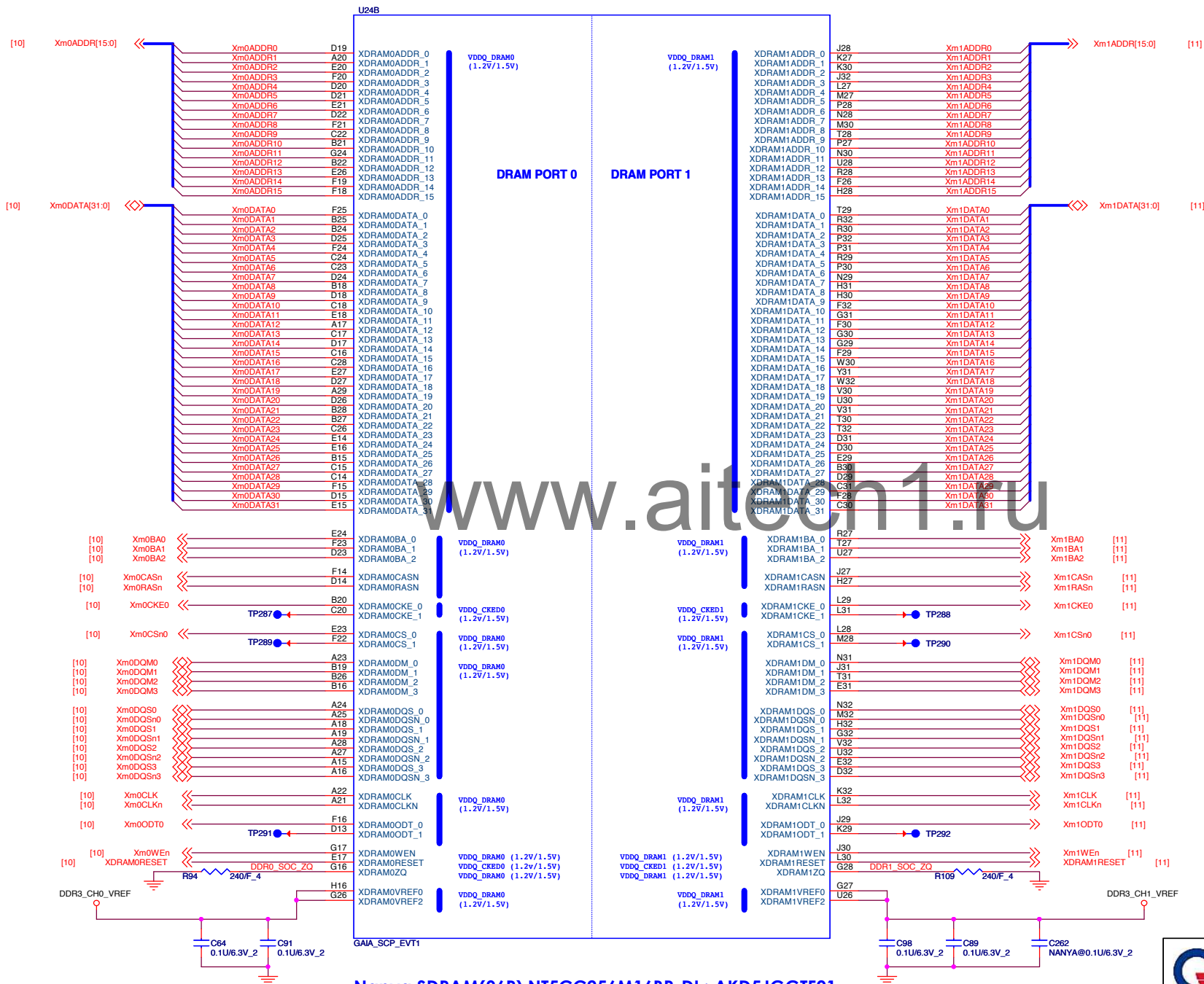
PROJECT : Y06 (MB)

Size Custom	Document Number CLOCKS & HOLELESS RESET	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 3 of 28	

[illegible]

OM[5:1] = 10011 : EMMC0 -> SDIO CH2



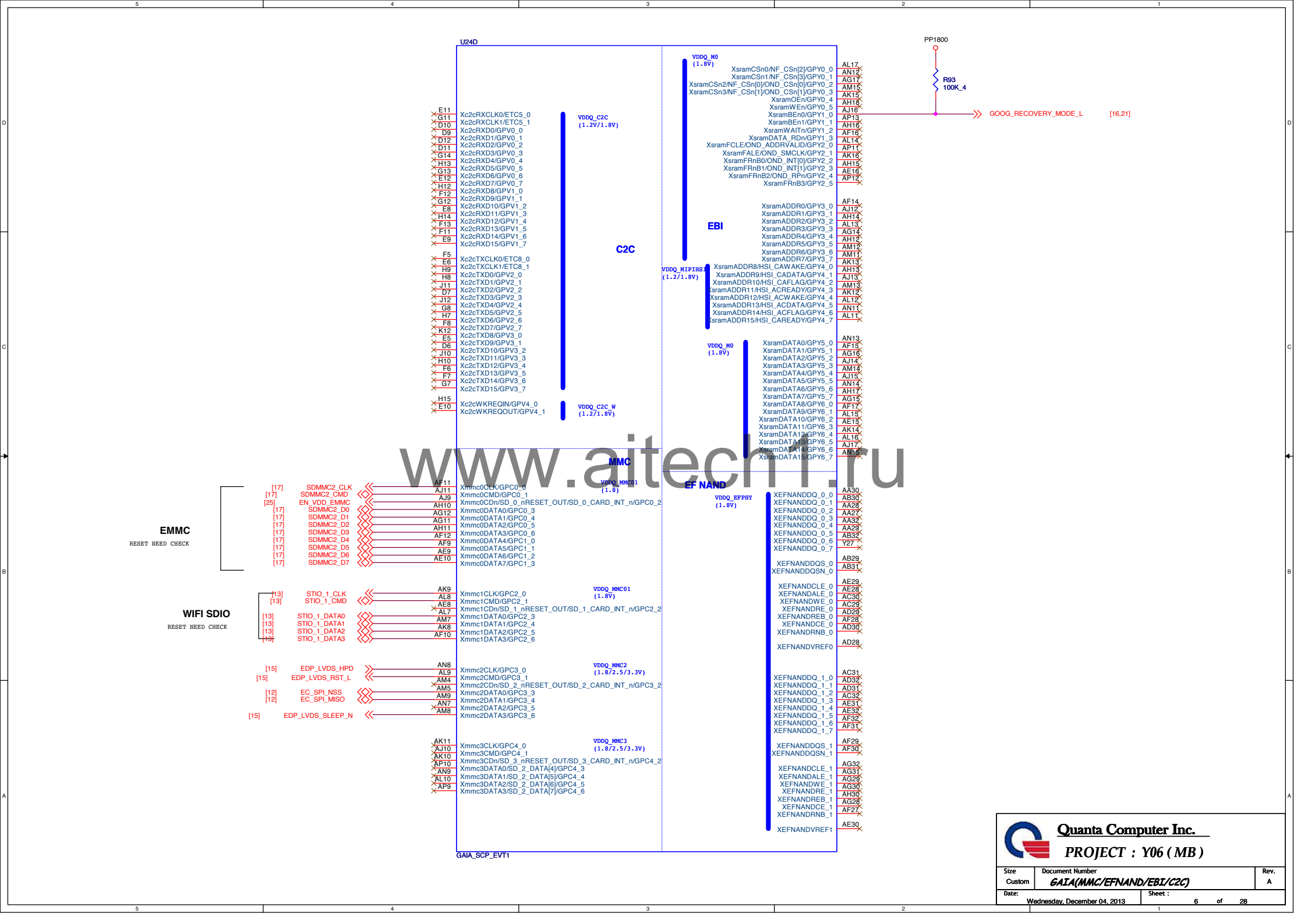


Nanya SDRAM(96P) NT5CC256M16BP-DI : AKD5JGGTF01
Micron SDRAM(96P) MT41K256M16HA-125:E : AKD5JGSTL11

Micron	C262
Nanya	STUFF

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PROJECT : Y06 (MB)

Size	Document Number	Rev.
Custom	GAIA_AP_2 (DRAM_XMO/XM1)	A
Date:	Wednesday, December 04, 2013	Sheet : 5 of 28



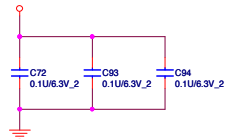
200mA
LDO2 1.2V/450mA/ON

5mA
BUCK5 1.2V/1.5A/ON
LDO2 1.2V/450mA/ON

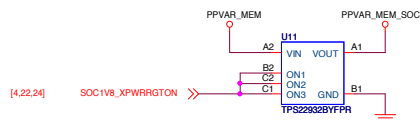
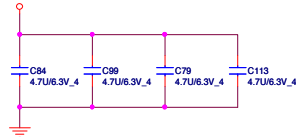
200mA

5mA
BUCK5 1.2V/1.5A/ON

PPVAR_MEM_SOC



PPVAR_MEM_SOC



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U24F

H17 VDDQ_DRAM0
H18 VDDQ_DRAM0
H19 VDDQ_DRAM0
H20 VDDQ_DRAM0
H21 VDDQ_DRAM0
H22 VDDQ_DRAM0
H23 VDDQ_DRAM0
J24 VDDQ_DRAM0
J25 VDDQ_DRAM0
F17 VDDQ_CKED0
H26 VDDQ_DRAM1
K26 VDDQ_DRAM1
L26 VDDQ_DRAM1
M26 VDDQ_DRAM1
N26 VDDQ_DRAM1
P26 VDDQ_DRAM1
R26 VDDQ_DRAM1
T26 VDDQ_DRAM1
T26 VDDQ_CKED1

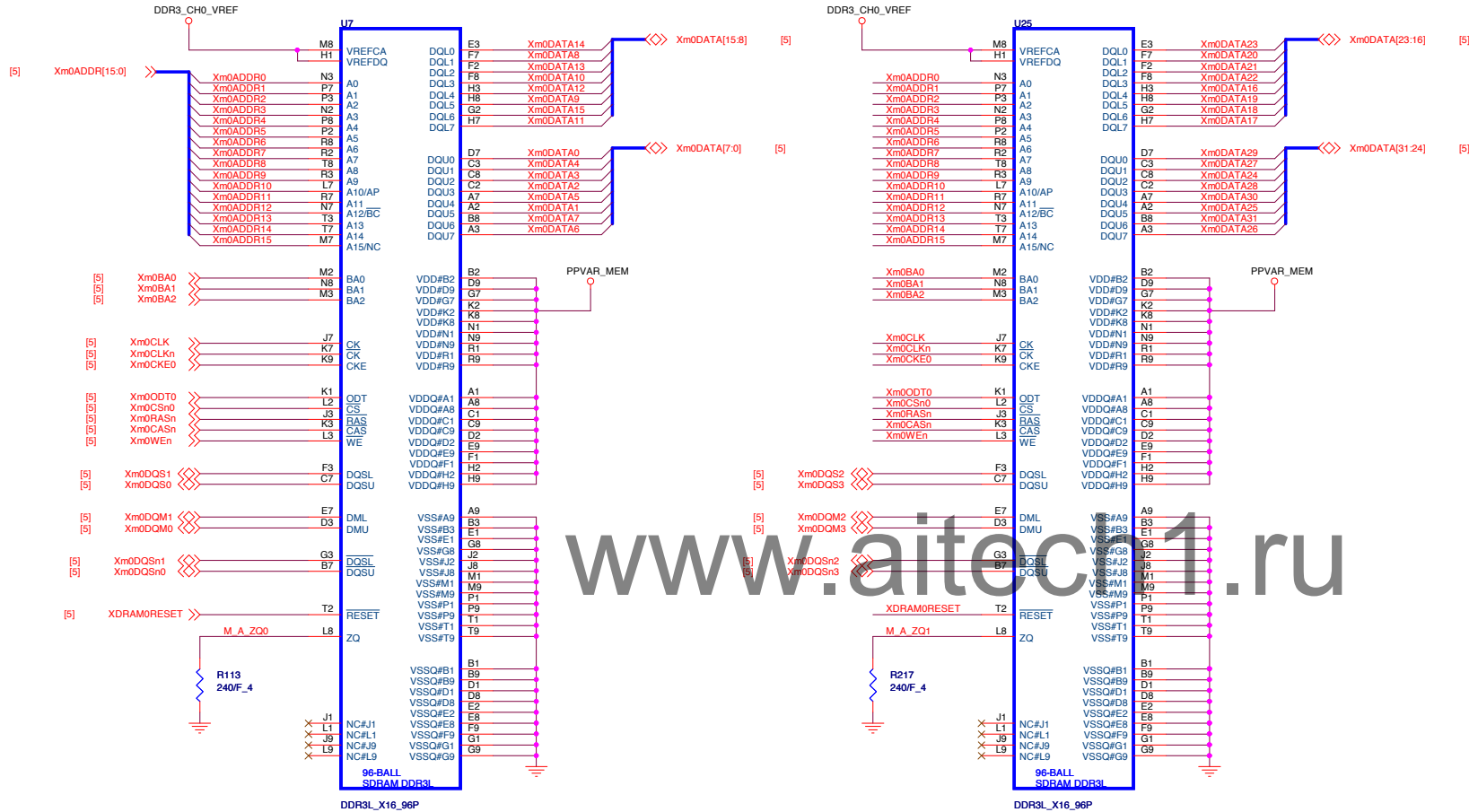
AA5 VSS_ADC0
AD5 VSS_ADC0
AC5 VSS_ADC1
W29 VSS_APLL
V26 VSS_BPLL
W26 VSS_CPLL
C10 VSS_DP
C12 VSS_DP
F9 VSS_DP
F10 VSS_DP
AB27 VSS_EPLL
Y26 VSS_GPLL
C4 VSS_HDMI
CS VSS_HDMI
C6 VSS_HDMI
A11 VSS_HSIC
A12 VSS_HSIC
L4 VSS_MIP0
M3 VSS_MIP0
P3 VSS_MIP0
G4 VSS_MIP1
H5 VSS_MIP1
V29 VSS_MIP1
AF5 VSS_MPLL
AG5 VSS_SATA
AH5 VSS_SATA
AH7 VSS_UHOST
AH9 VSS_USB3
AN5 VSS_USB3
AN6 VSS_USB3
Y28 VSS_VPLL
AD26 VSS_VPLL
AD26 VSSO_EFFHY
V10 VSSO_EFFHY
V13 VSS
V25 VSS
W17 VSS
W10 VSS
W13 VSS
W16 VSS
W17 VSS
W18 VSS
W19 VSS
W20 VSS
W21 VSS
W22 VSS
W23 VSS
W24 VSS
W25 VSS
W31 VSS
Y7 VSS
Y10 VSS
Y13 VSS
Y16 VSS
AA10 VSS
AA13 VSS
AA16 VSS
AA31 VSS
AB10 VSS
AB13 VSS
AB16 VSS
AB17 VSS
AB18 VSS
AB19 VSS
AB22 VSS
AB23 VSS
AB24 VSS
AB25 VSS
AC10 VSS
AC13 VSS
AC16 VSS
AC26 VSS
AD8 VSS
AD9 VSS
AD10 VSS
AD13 VSS
AD15 VSS
AD16 VSS
AD25 VSS
AE3 VSS
AE7 VSS
AE11 VSS
AE12 VSS
AE13 VSS
AE14 VSS
AE17 VSS
AE18 VSS
AE19 VSS
AE20 VSS
AE21 VSS
AE22 VSS
AE23 VSS
AE24 VSS
AF7 VSS
AM1 VSS
AM32 VSS
AN1 VSS
AN2 VSS
AN31 VSS
AN32 VSS

GAIA_SCP_EV11

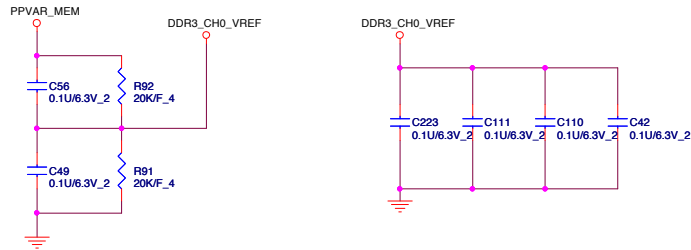
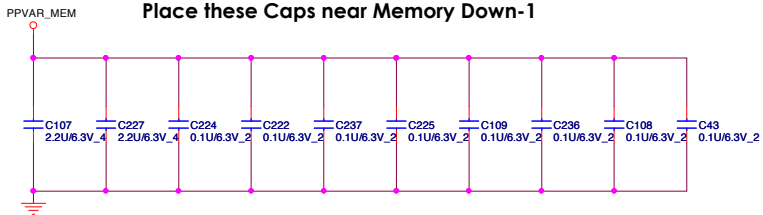
A1 VSS
A2 VSS
A3 VSS
A4 VSS
A14 VSS
A26 VSS
A30 VSS
A31 VSS
A32 VSS
B1 VSS
B2 VSS
B8 VSS
B14 VSS
B17 VSS
B23 VSS
B29 VSS
B31 VSS
B32 VSS
C1 VSS
C11 VSS
C13 VSS
C19 VSS
C21 VSS
C25 VSS
C27 VSS
C29 VSS
C32 VSS
D16 VSS
D28 VSS
E7 VSS
E13 VSS
E19 VSS
E22 VSS
E25 VSS
E28 VSS
E30 VSS
F27 VSS
F31 VSS
G15 VSS
G25 VSS
H11 VSS
H29 VSS
J7 VSS
J8 VSS
J9 VSS
J16 VSS
J17 VSS
J18 VSS
J19 VSS
J20 VSS
J21 VSS
J22 VSS
J23 VSS
K6 VSS
K9 VSS
K10 VSS
K11 VSS
K13 VSS
K14 VSS
K15 VSS
K24 VSS
K28 VSS
K31 VSS
L6 VSS
L9 VSS
L12 VSS
L15 VSS
L24 VSS
M6 VSS
M9 VSS
M12 VSS
M15 VSS
M16 VSS
M17 VSS
M18 VSS
M21 VSS
M22 VSS
M23 VSS
M25 VSS
M29 VSS
M31 VSS
N6 VSS
N9 VSS
N12 VSS
N15 VSS
N24 VSS
N27 VSS
P6 VSS
P9 VSS
P12 VSS
P15 VSS
P24 VSS
P29 VSS
R7 VSS
R12 VSS
R15 VSS
R16 VSS
R17 VSS
R18 VSS
R21 VSS
R22 VSS
R23 VSS
R29 VSS
R31 VSS
R7 VSS
T16 VSS
T24 VSS
U7 VSS
U17 VSS
U18 VSS
U19 VSS
U20 VSS
U21 VSS
U22 VSS
U23 VSS
U24 VSS
U25 VSS
U29 VSS
U31 VSS
Y7 VSS
AP1 VSS
AP2 VSS
AP3 VSS
AP30 VSS
AP31 VSS
AP32 VSS

DDR3L Port0

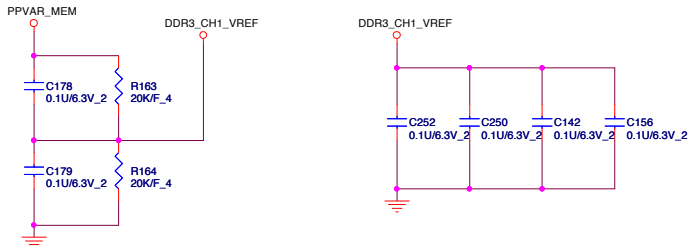
Nanya SDRAM(96P) NT5CC256M16BP-DI : AKD5JGGTF01
Micron SDRAM(96P) MT41K256M16HA-125:E : AKD5JGSTL11



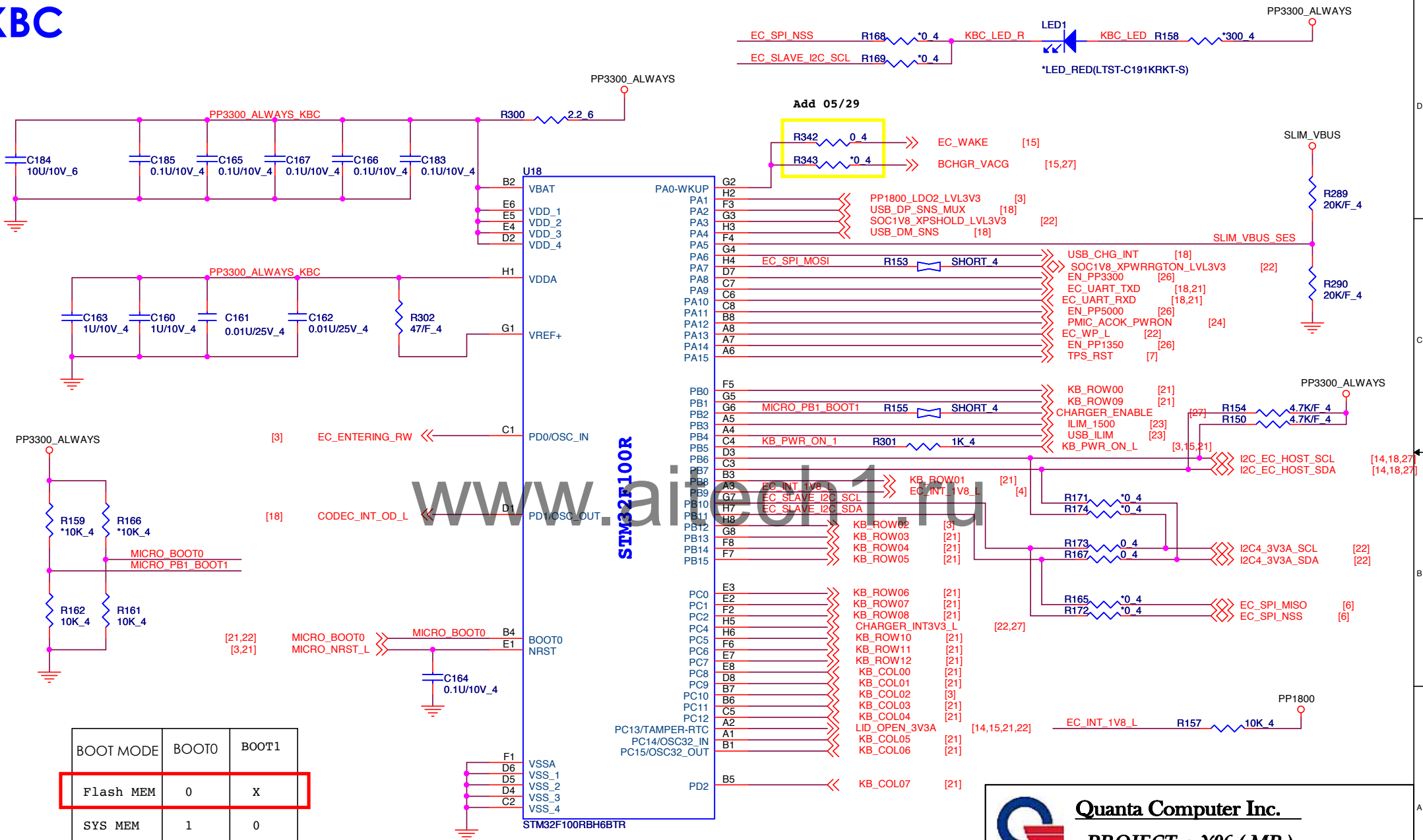
Place these Caps near Memory Down-1




Nanya SDRAM(96P) NT5CC256M16BP-DI : AKD5JGGTF01
Micron SDRAM(96P) MT41K256M16HA-125:E : AKD5JGSTL11



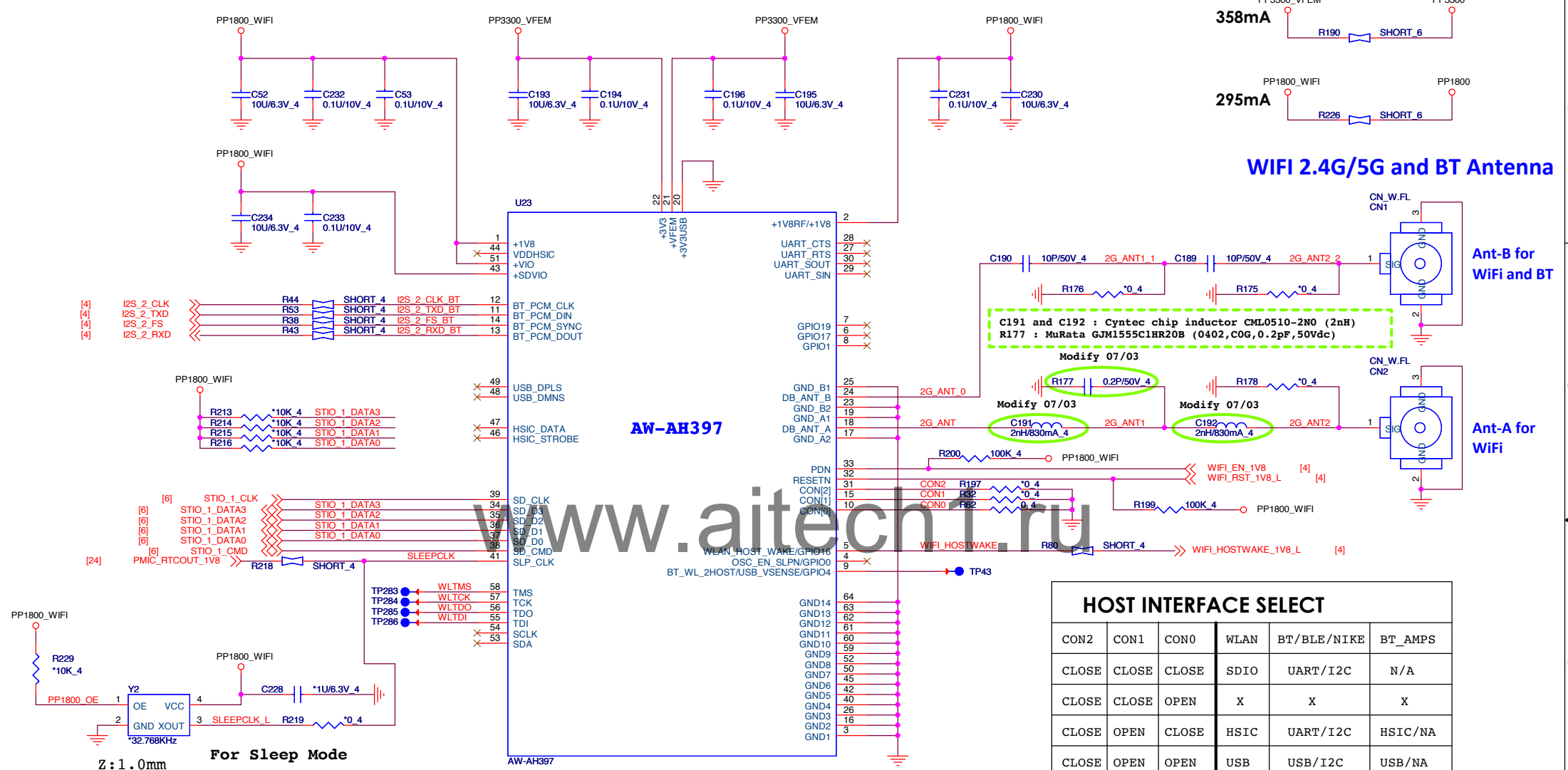
KBC



BOOT MODE	BOOT0	BOOT1
Flash MEM	0	X
SYS MEM	1	0
SRAM	1	1


 Quanta Computer Inc. PROJECT : Y06 (MB)		
Size Custom	Document Number Keyboard/Power Controller	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 12 of 28	

WiFi (AW-AH397)



HOST INTERFACE SUPPLY CONFIG				
SIGNAL NAME	+3V3USB	+SDVIO	VDDHSIC *	+1V8HSIC
SDIO	GND	VIO	FLOAT	FLOAT
USB	3.3V	FLOAT	FLOAT	FLOAT
HSIC	GND	FLOAT	LVLD050_VOUT	1.8V

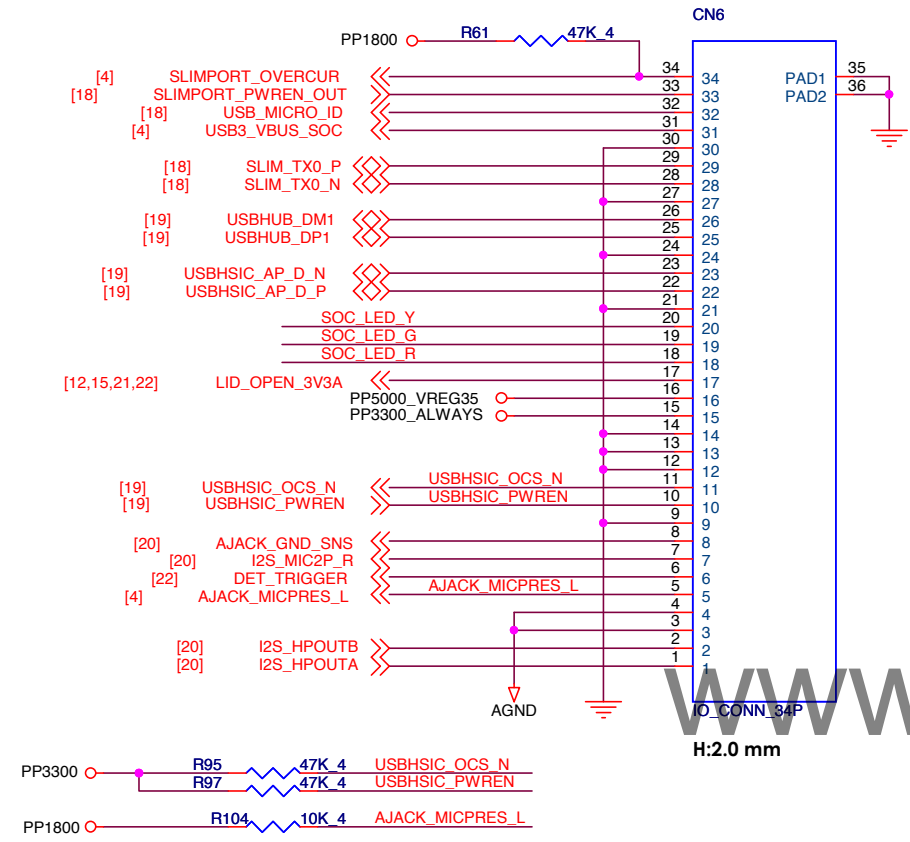
NOTE * : VDDHSIC IS SUPPLIED INTERNALLY BY LVLD050_VOUT



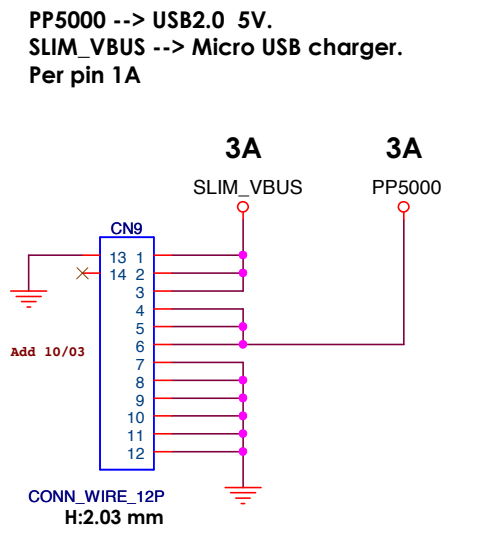
Quanta Computer Inc.
PROJECT : Y06 (MB)

Size	Document Number	Rev.
Custom	WIFI (AW-AH397)	A
Date:	Wednesday, December 04, 2013	Sheet : 13 of 28

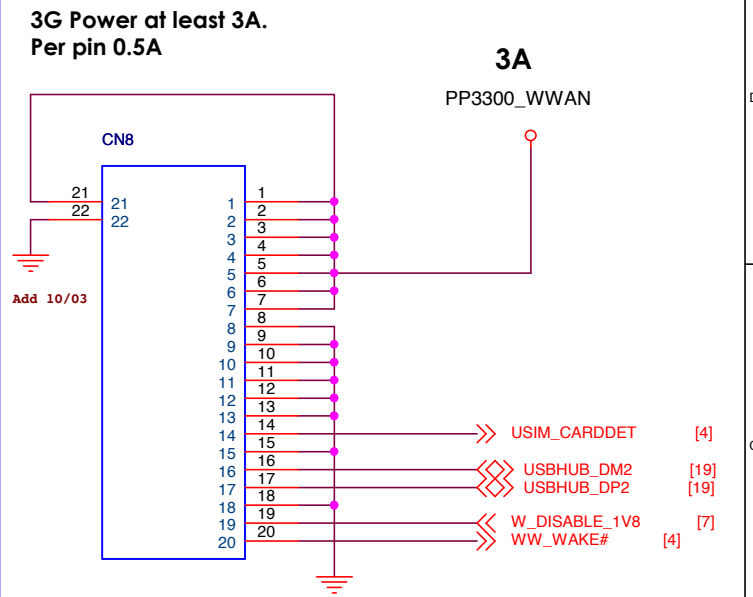
IO/B FPC Connector



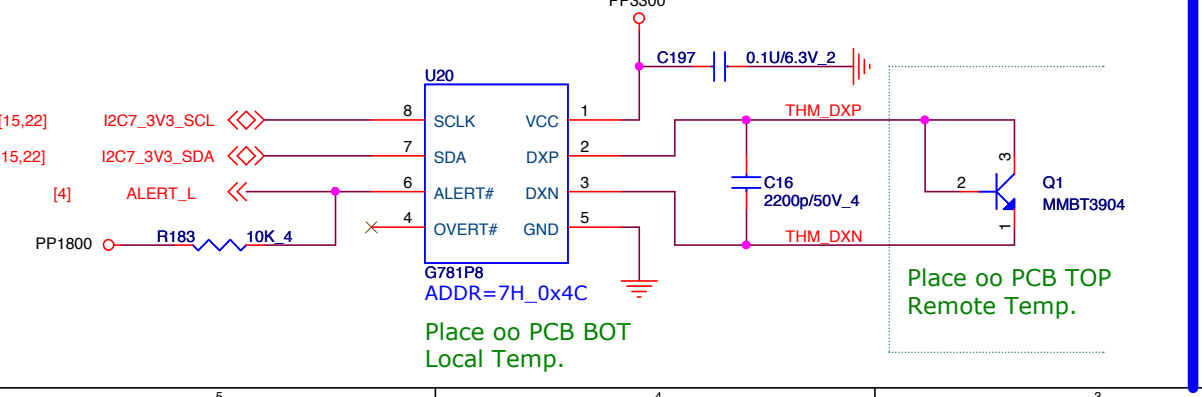
IO/B PWR Wire Conn.



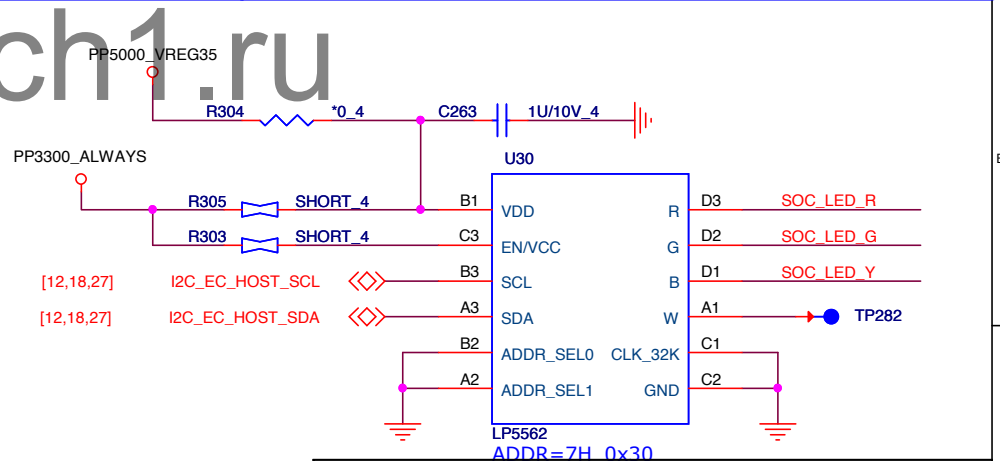
WWAN/B FFC Conn.



Thermal Sensor



LED driver



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PROJECT : Y06 (MB)

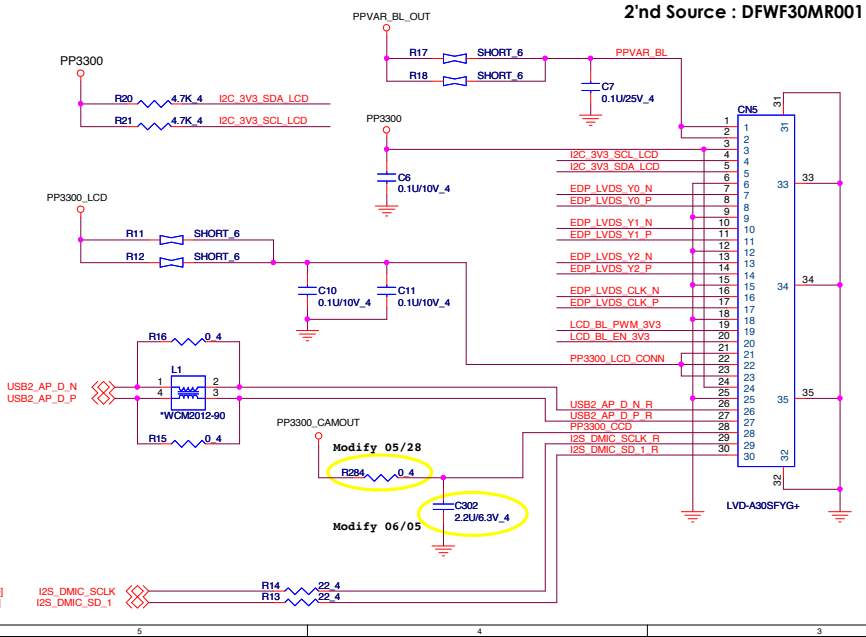
Size Custom	Document Number IO Conn/Thermal/LED drive	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 14 of 28	

DP to LVDS bridge

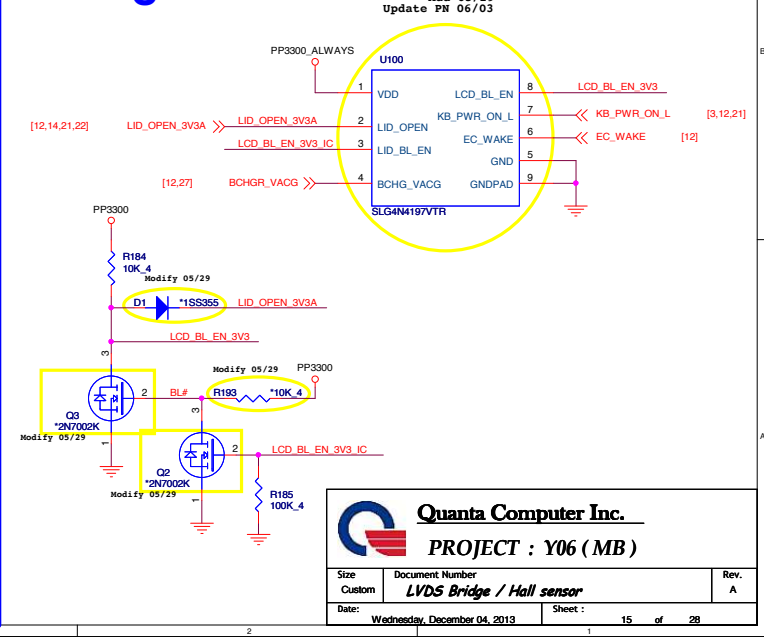
Power On Configuration

(EDP LVDS_CFG1) -> Choose M
I2C_CFG: Initial code loading selection, internal pull-down ~80K
L: Reserved
M: No initial code loading, external I2C control is expected
H: Load initial code from external EEPROM through MSL/MSDA
(EDP LVDS_CFG3) -> Choose M
RLV_CFG: LVDS color depth and data mapping selection, internal pull-down ~80K
L: 8-bit LVDS, VESA mapping
M: 8-bit LVDS, JEIDA mapping
H: 8-bit LVDS, both VESA and JEIDA mapping
(EDP LVDS_CFG3) -> Choose M
RLV_SSC: LVDS SSC selection, internal pull-down ~80K
L: SSC off
M: +/- 0.5% central spreading
H: +/- 1% central spreading
(EDP LVDS_CFG4) -> Choose L
I2C_ADDR: I2C Slave address selection, internal pull-down ~80K
L: 7H'0x08 ~ 7H'0x0Fh
H: 7H'0x48 ~ 7H'0x4Fh

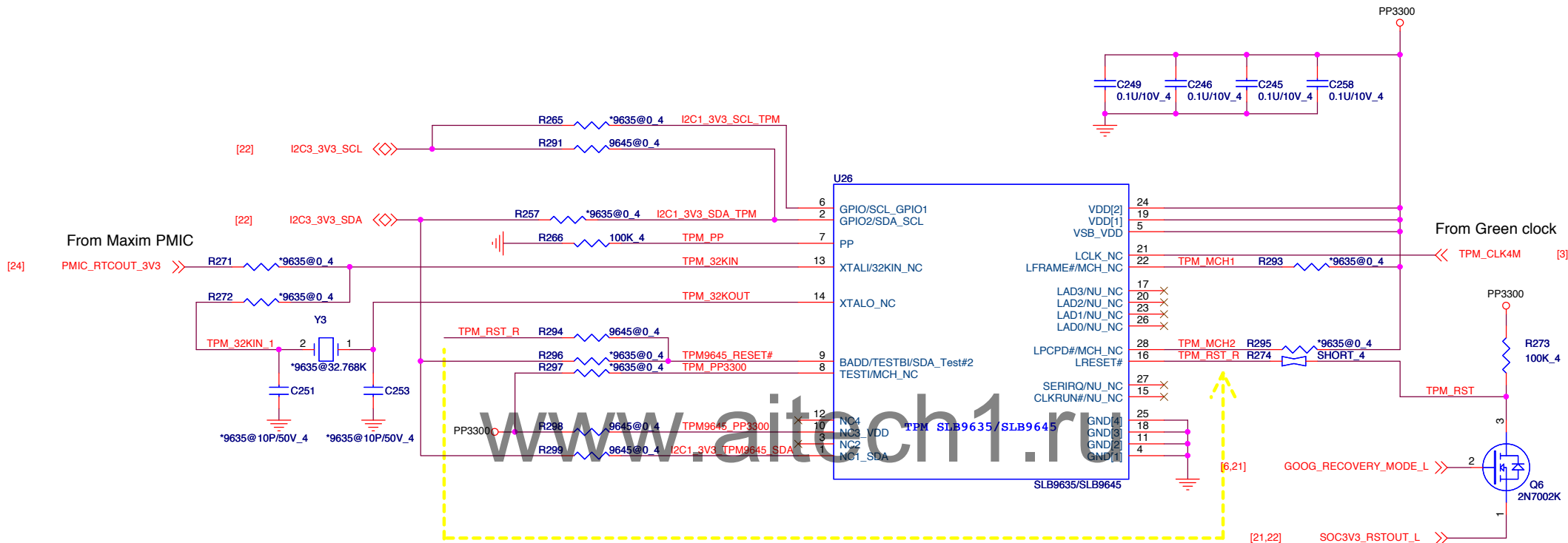
LVDS CONN



Backlight Control




TPM



TPM option	Product
9635@	SLB9635TT1.2
9645@	SLB9645TT1.2 FW 133.32 GOOG

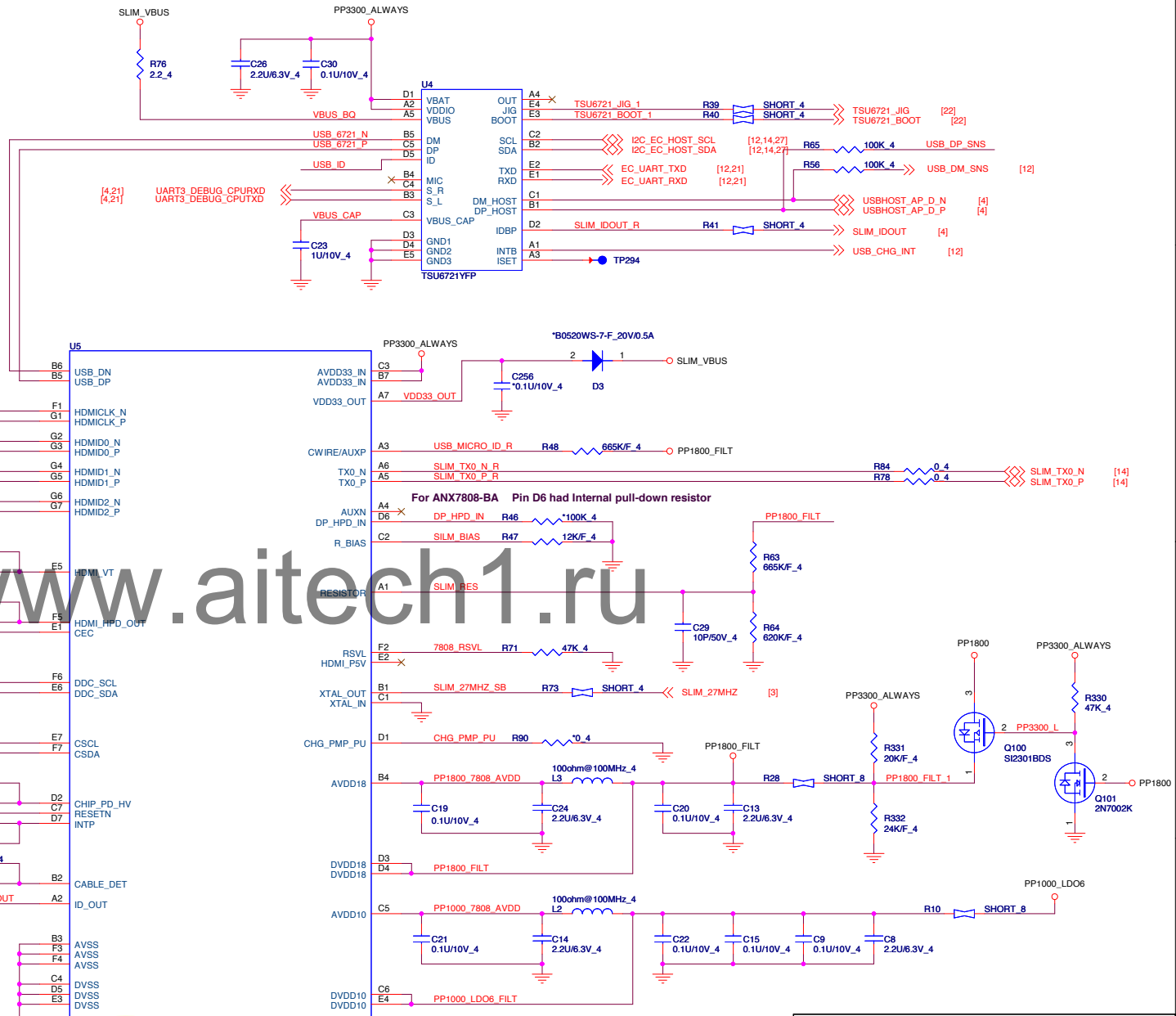
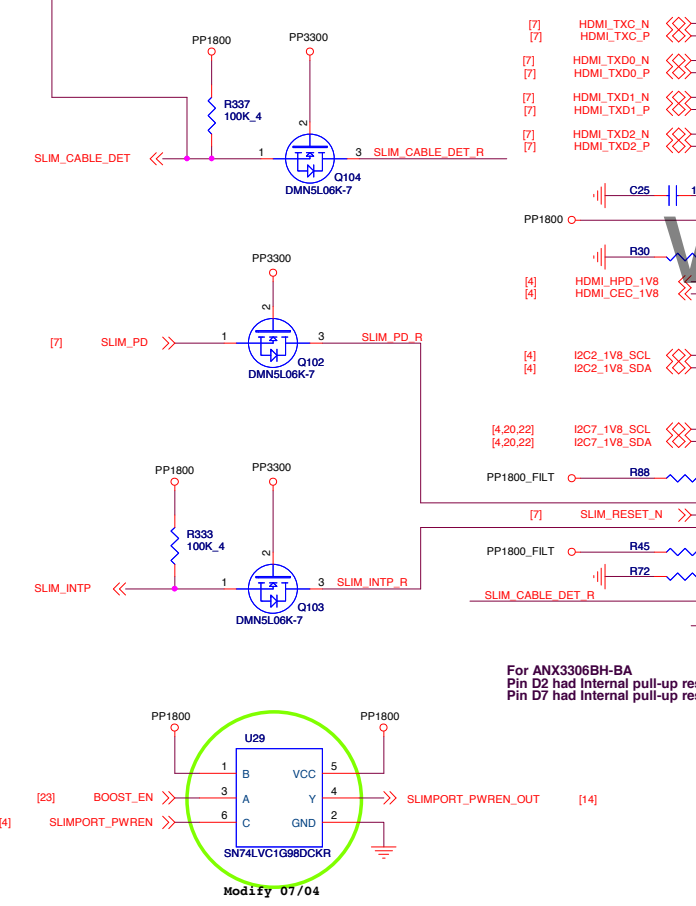
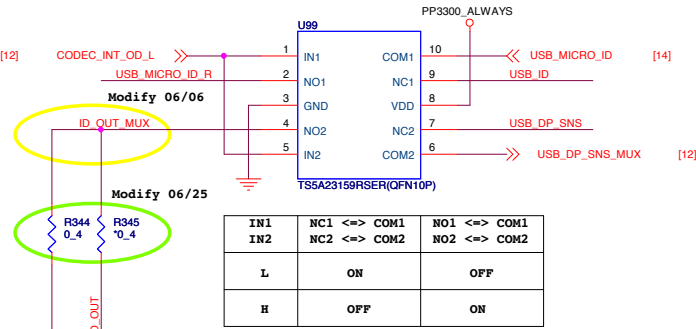
SLB9635 : AL009635K10
SLB9645 : AR00C1D2000


Pin name definition:
9635 pin function_9645 pin function
Example:
BADD/TESTBI/SDA_Test#2/SDA
9635 pin9 function: BADD/TESTBI/SDA
9645 pin9 function: Test#2/SDA

 Quanta Computer Inc. PROJECT : Y06 (MB)		
Size Custom	Document Number TPM	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 16 of 28	

VIDEO OVER USB

MUX



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PROJECT : Y06 (MB)

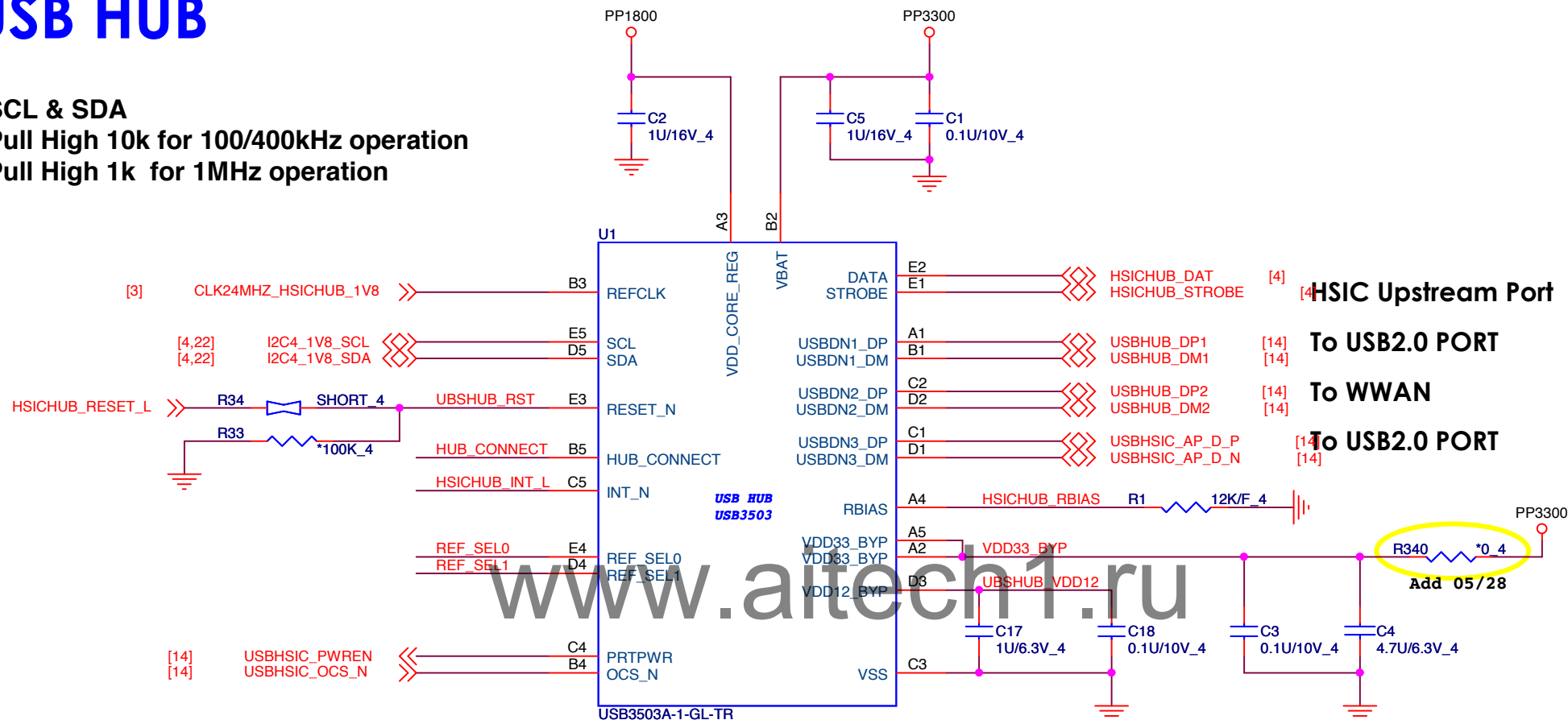
Size	Document Number	Rev.
Custom	VIDEO OVER USB	A
Date:	Wednesday, December 04, 2013	Sheet : 18 of 28

USB HUB

SCL & SDA

Pull High 10k for 100/400kHz operation

Pull High 1k for 1MHz operation



INT_N Pull GND. (Pull high will use Primary Clock.)

Secondary Reference Clock Frequencies

REF_SEL[1:0]	Frequency (MHz)
'00'	24.0
'01'	27.0
'10'	25.0
'11'	50.0

Select REF_SEL[1:0] = 00 = 24MHz.

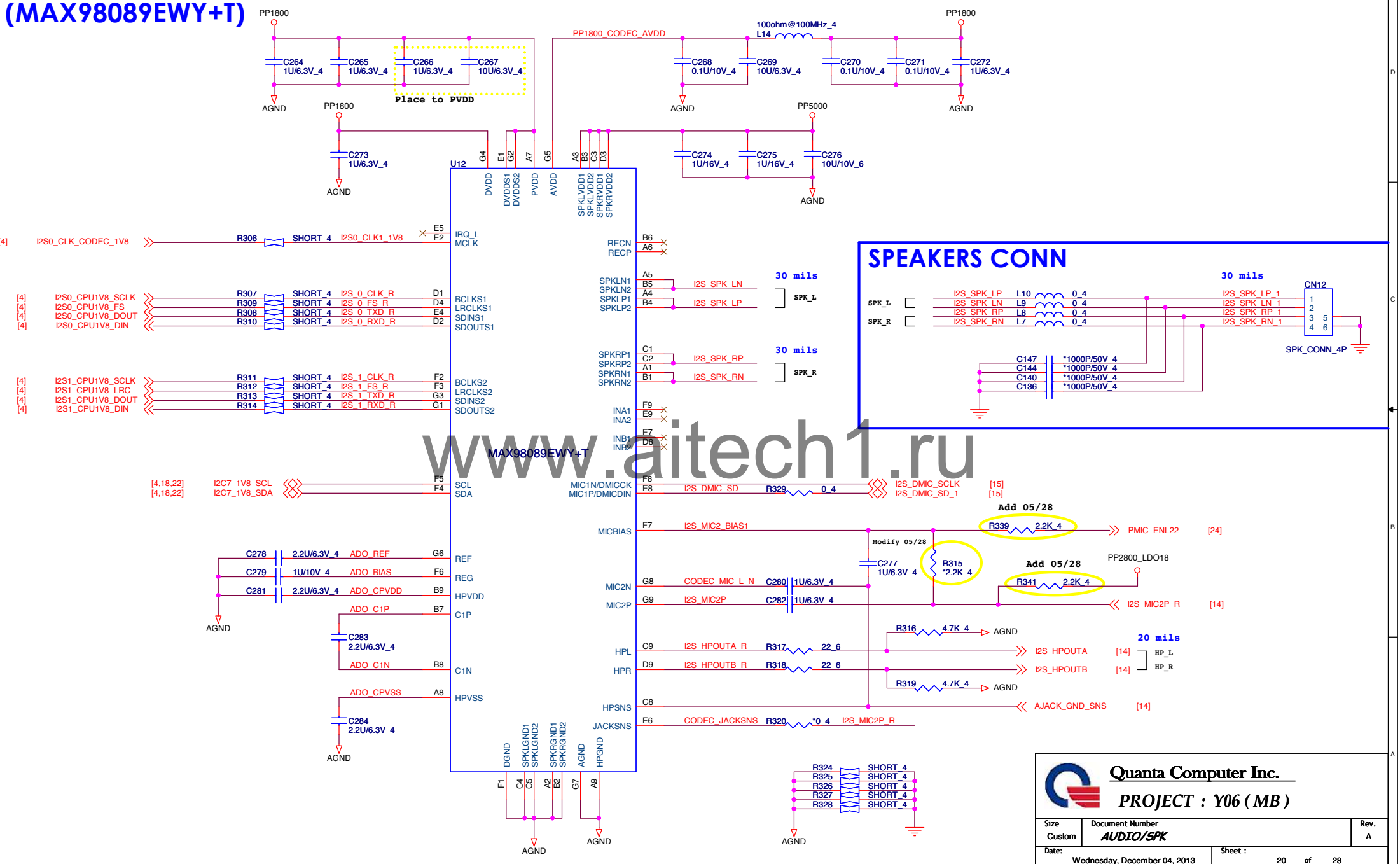



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PROJECT : Y06 (MB)

Size Custom	Document Number USB HUB	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 19 of 28	

Audio Codec (MAX98089EWY+T)

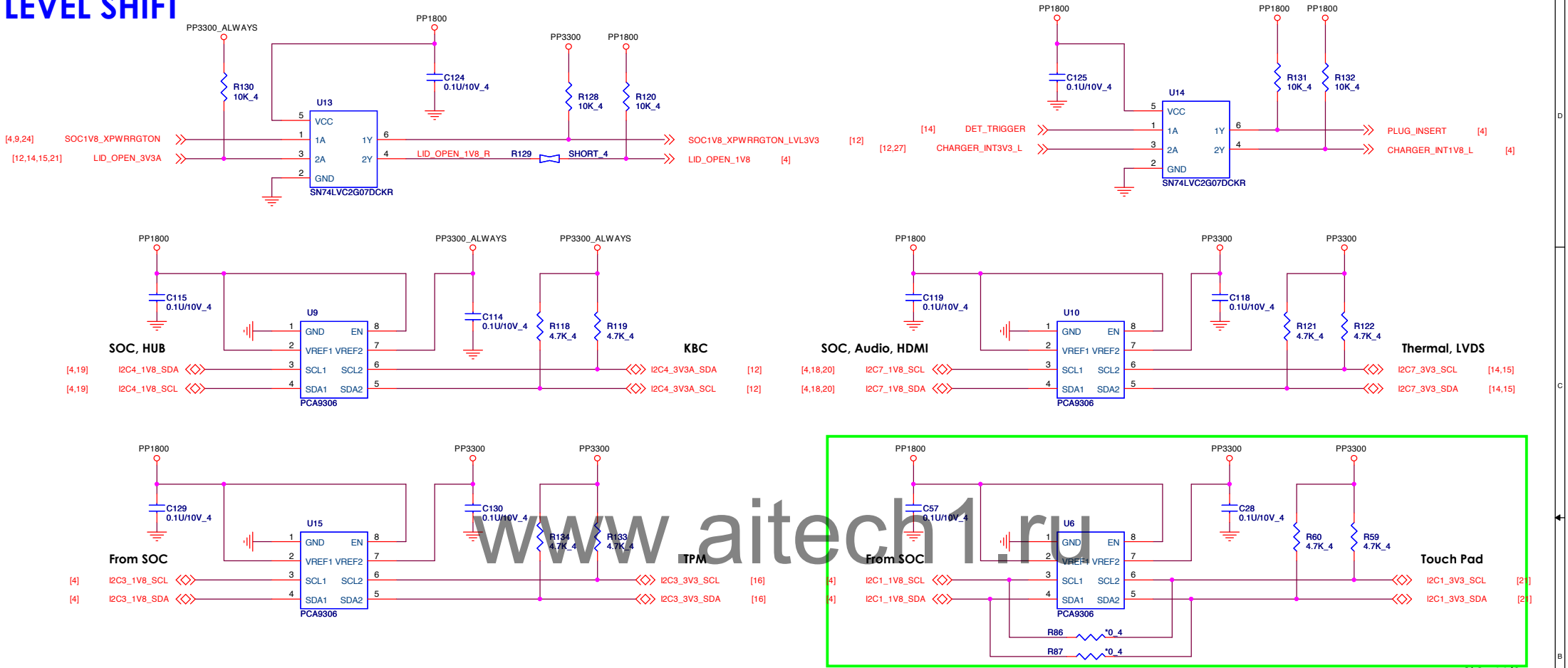




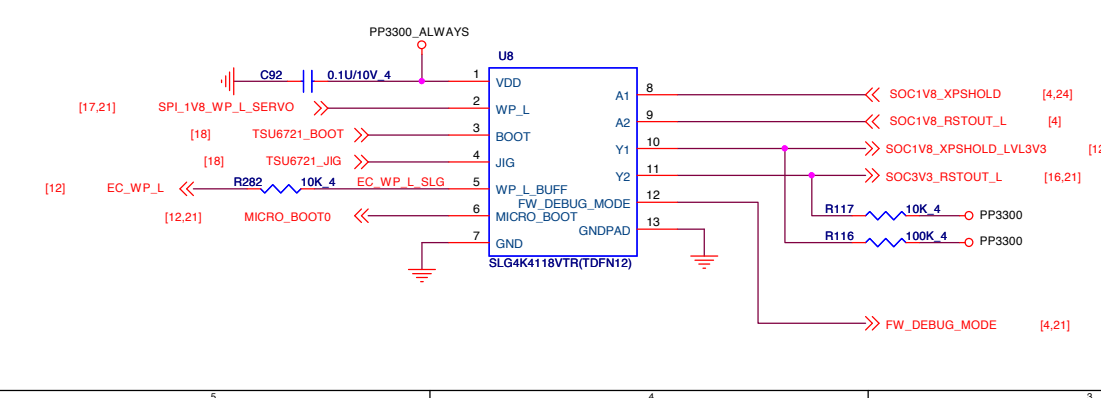
Quanta Computer Inc.
PROJECT : Y06 (MB)

Size Custom	Document Number AUDIO/SPK	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 20 of 28	

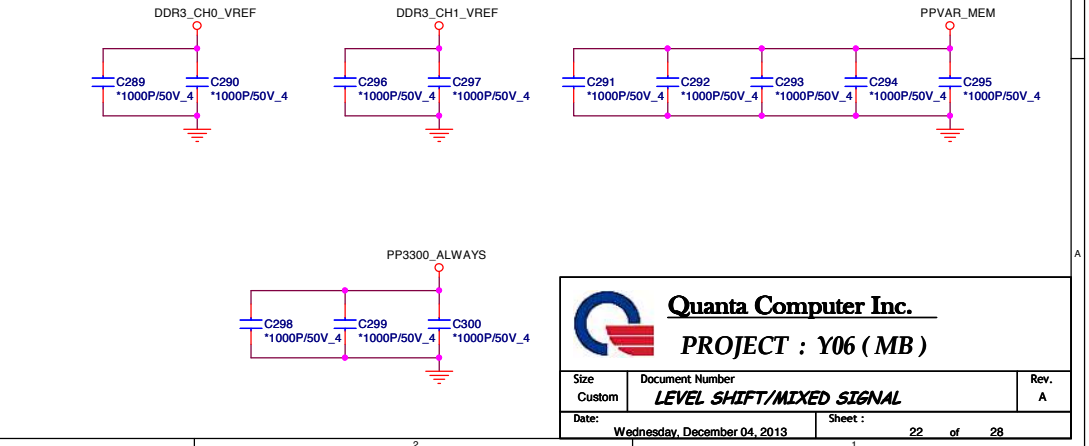
LEVEL SHIFT



Logics and Level Shifters (Green Pak II)

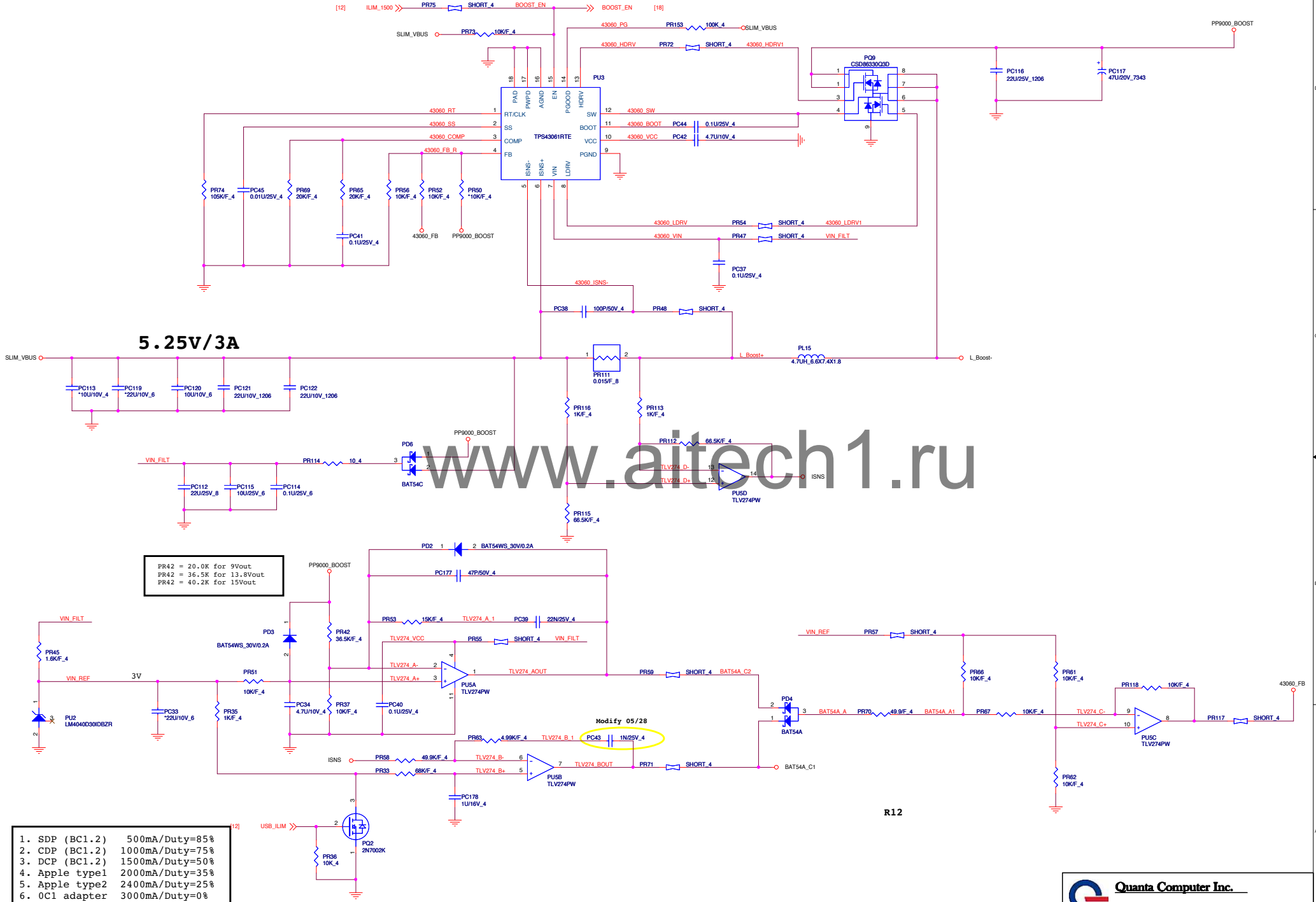


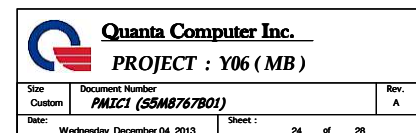
EMI



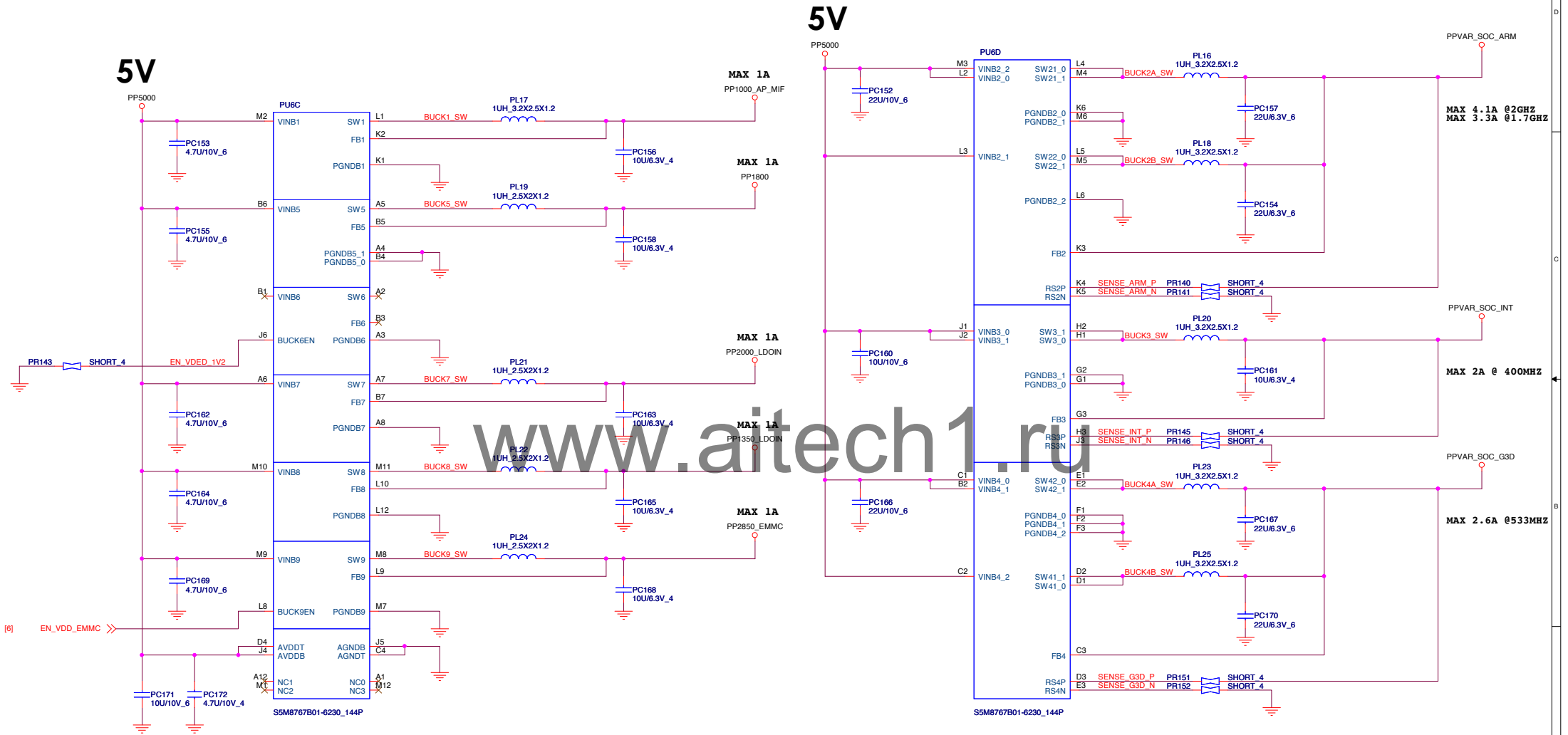
USB BOOST


13.8V for 3S1P





PMIC2



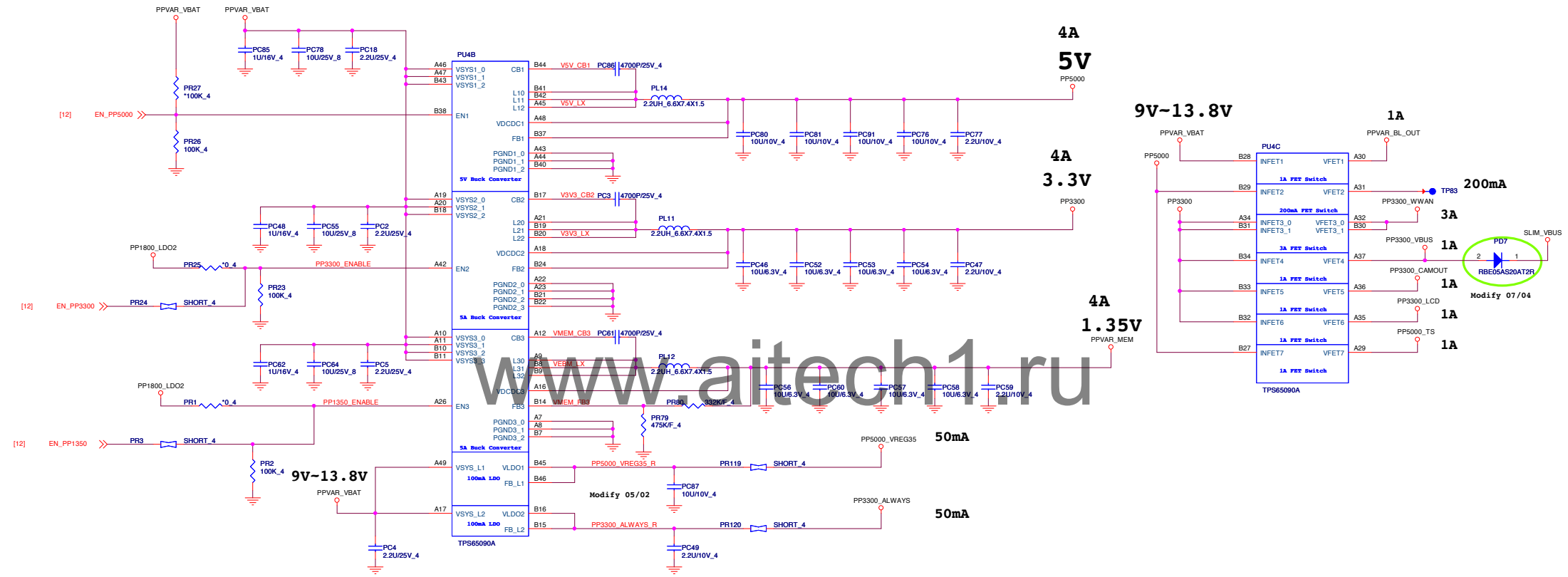


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PROJECT : Y06 (MB)

Size Custom	Document Number PMIC2 (S5M8767B01)	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 25 of 28	

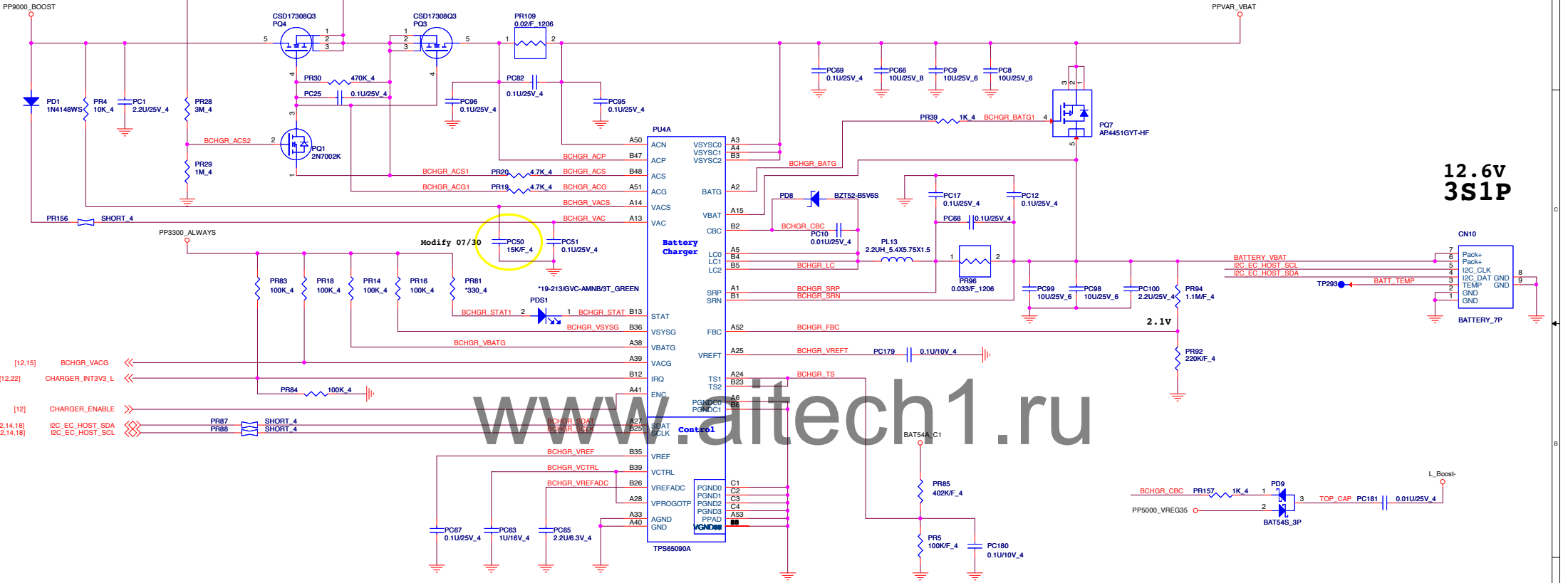
9V-13.8V




PMU Battery Charger

13.8V

9V-13.8V



12.6V
3S1P

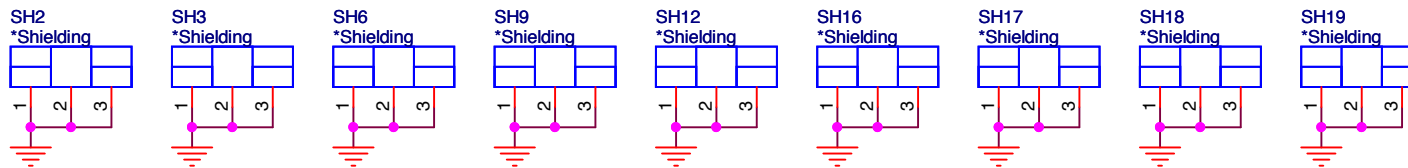


Quanta Computer Inc.
PROJECT : Y06 (MB)

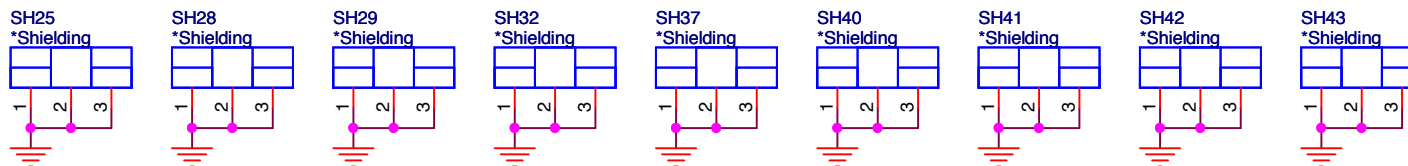
Size	Document Number	Rev.
Custom	PMU Battery Charger	A
Date:	Wednesday, December 04, 2013	Sheet : 27 of 28

Shielding set

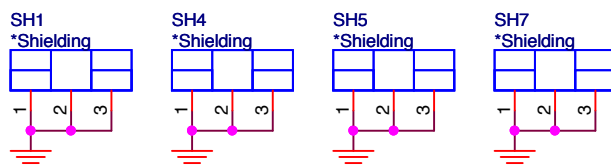
CPU
(TOP)



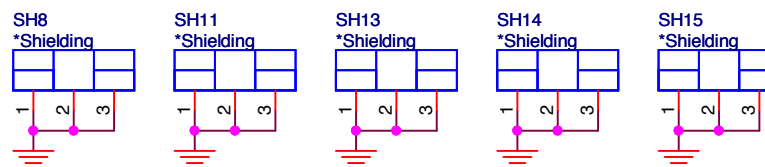
CPU
(BOT)



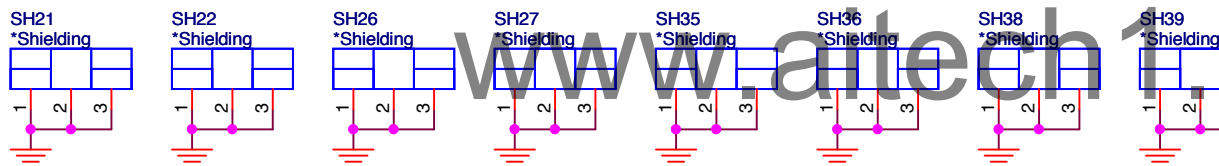
eMMC
(TOP)



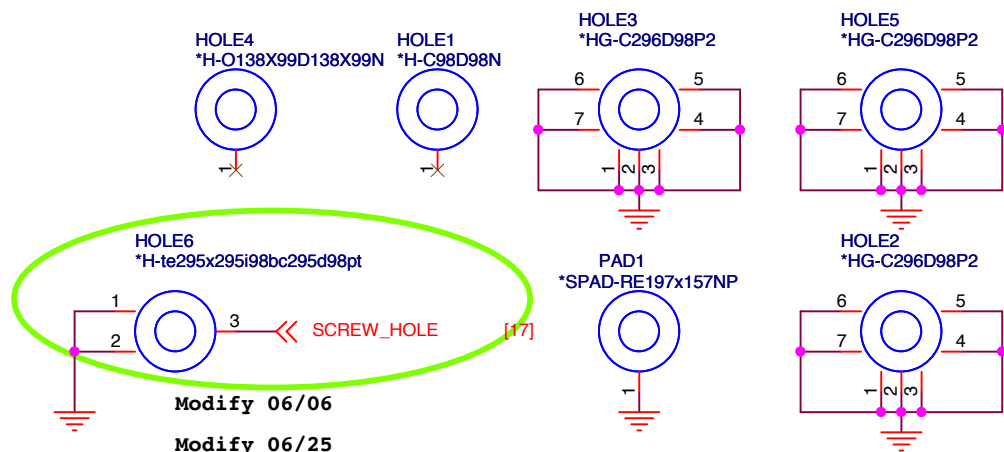
PMIC
(TOP)



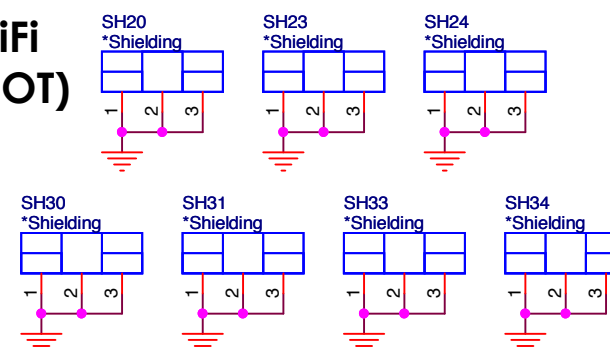
PMU
(BOT)



Screw Hole



WiFi
(BOT)



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PROJECT : Y06 (MB)

Size Custom	Document Number SCREW & SHIELD	Rev. A
Date: Wednesday, December 04, 2013	Sheet : 28 of 28	

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